

10/534946

JC20 PCT/PTO 11 MAY 2005

94326

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application )  
Applicant: Takashi YOKOYAMA et al. )  
Serial No.: Unassigned )  
Filed: May 11, 2005 )  
For: SILICON WAFER, ITS MANUFACTURING METHOD, )  
AND ITS MANUFACTURING APPARATUS )

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313

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Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. The cited references are enclosed herewith.

Documents for which the supplied date of publication lists the year of publication without the month were published sufficiently earlier than the effective U.S. filing date and any foreign priority date, so that the particular month of publication is not in issue. Pursuant to §609 of the MPEP, it is understood that the month of publication is not required when the particular month of publication is not in issue.

No inferences should be drawn that the attached list represents a comprehensive investigation, or that any material disclosed is equivalent to the subject invention. In addition, none of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

Applicant also encloses a copy of the PCT Search Report and references cited therein.

The cited document(s) disclose numerous specific features. There has been no attempt to list each

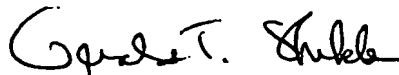
Atty. Docket: 94326

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Respectfully submitted,



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Gerald T. Shekleton  
Registration No. 27,466

Dated: May 11, 2005

**WELSH & KATZ, LTD.**

120 South Riverside Plaza  
22nd Floor  
Chicago, Illinois 60606  
Telephone: 312-655-1500

PTO/SB/08A &amp; (04-03)

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				Application Number Unassigned Filing Date May 11, 2005 First Named Inventor YOKOYAMA et al. Art Unit Unassigned Examiner Name Unassigned	
Sheet	1	of	1	Attorney Docket Number 94326	

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Column, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> -Number <sup>4</sup> -Kind Code <sup>3</sup> (if known)				
/MS/		JP 2001-278692	10/10/2001	Ryoji et al.		Abstract
/MS/		JP 61-193456	08/27/1986	Kikuo et al.		Abstract
/MS/		JP 2001-102385	04/13/2001	Kazuhiro et al.		Abstract
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application

Applicant: Takashi YOKOYAMA et al.

Serial No.: 10/534,946

Filed: May 11, 2005

For: SILICON WAFER, ITS MANUFACTURING  
METHOD, AND ITS MANUFACTURING  
APPARATUS

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) Gerald T. Shekleton Reg. No. 27,466

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Serial No.: 10/534,946

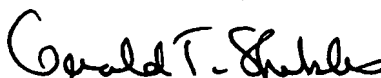
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Respectfully submitted,

WELSH & KATZ, LTD.



Gerald T. Shekleton  
Registration No. 27,466

Dated: June 1, 2005  
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Telephone: 312/655-1500

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/534,946
		Filing Date	May 11, 2005
		First Named Inventor	YOKOYAMA et al.
		Art Unit	Unassigned
		Examiner Name	Unassigned
Sheet 1 of 1	Attorney Docket Number	94326	

U.S. PATENT DOCUMENTS					
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		JP 2001-261495	09/26/2001	Kozo et al.		Abstract
		JP 8-330316	12/13/1996	Masataka et al.		Abstract
		JP 11-199387	07/27/1999	Hideki et al.		Abstract
		JP 11-79889	03/23/1999			No
		JP 2000-159594	06/13/2000	Kozo et al.		Abstract
		JP 5345679	04/24/1978			No
		JP 54-119375	09/17/1979	Sadao et al.		Abstract
		JP 56-21758	02/28/1981			No
		JP 63-319288	12/27/1988	Hirotooshi		Abstract
		JP 2-172884	07/04/1990	Tomohiko et al.		Abstract
		JP 6-16490	01/25/1994			No
		JP 6-56572	03/01/1994			No
		JP 5-319976	12/03/1993	Rintarou et al.		Abstract
/MS/		JP 7-89789	04/04/1995			No

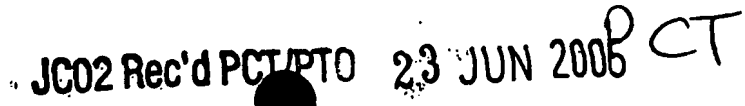
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/MS/		De Kock et al.; "The Effect of Doping on the Formation of Swirl defects in Dislocation-Free Czochralski-Grown Silicon Crystals," <i>Journal of Crystal Growth</i> , Vol. 49, 1980, pp. 718-734.	

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ENT 5

**In Re U.S. Patent Application**

Applicant: Takashi YOKOYAMA et al.

Serial No.: 10/534,946

**Filed: May 11, 2005**

For: SILICON WAFER, ITS MANUFACTURING  
METHOD, AND ITS MANUFACTURING  
APPARATUS

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**Gerald T. Shekleton Reg. No. 27,466**

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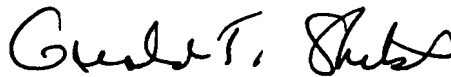
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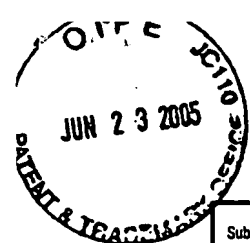
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